



# SCPH-7500 SERIES

## SERVICE MANUAL

*Japan Model*  
SCPH-7500

*US/Canada Model*  
SCPH-7501

*Australia Model*  
SCPH-7502A

*UK Model*  
SCPH-7502B

*AEP Model*  
SCPH-7502C

*Asian Model*  
SCPH-7503

3rd Edition

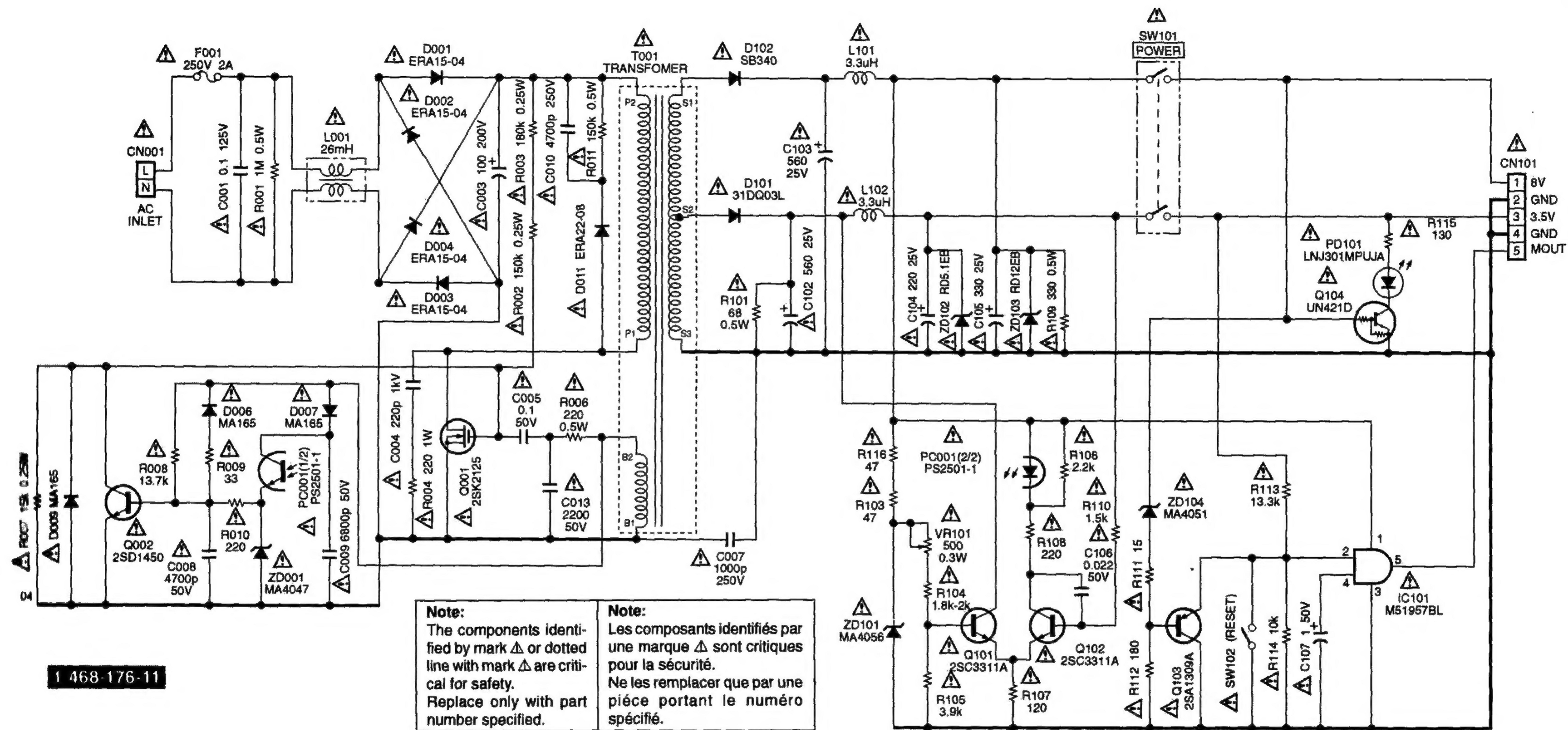


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Registered No.

# PlayStation



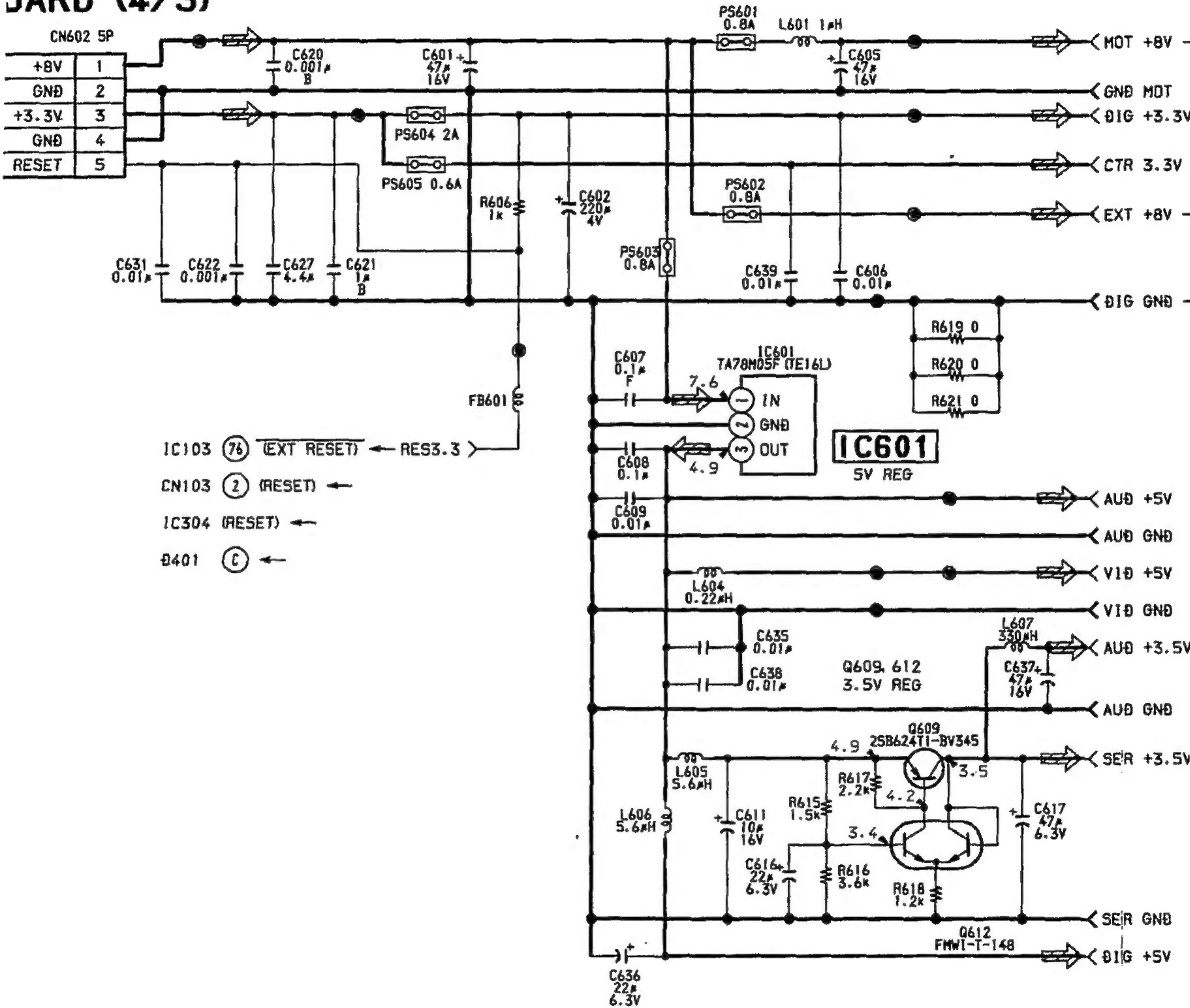


6-9. PRINTED WIRING BOARD (POWER BLOCK (1-468-176-11))

POWER BLOCK (SCPH-7500)



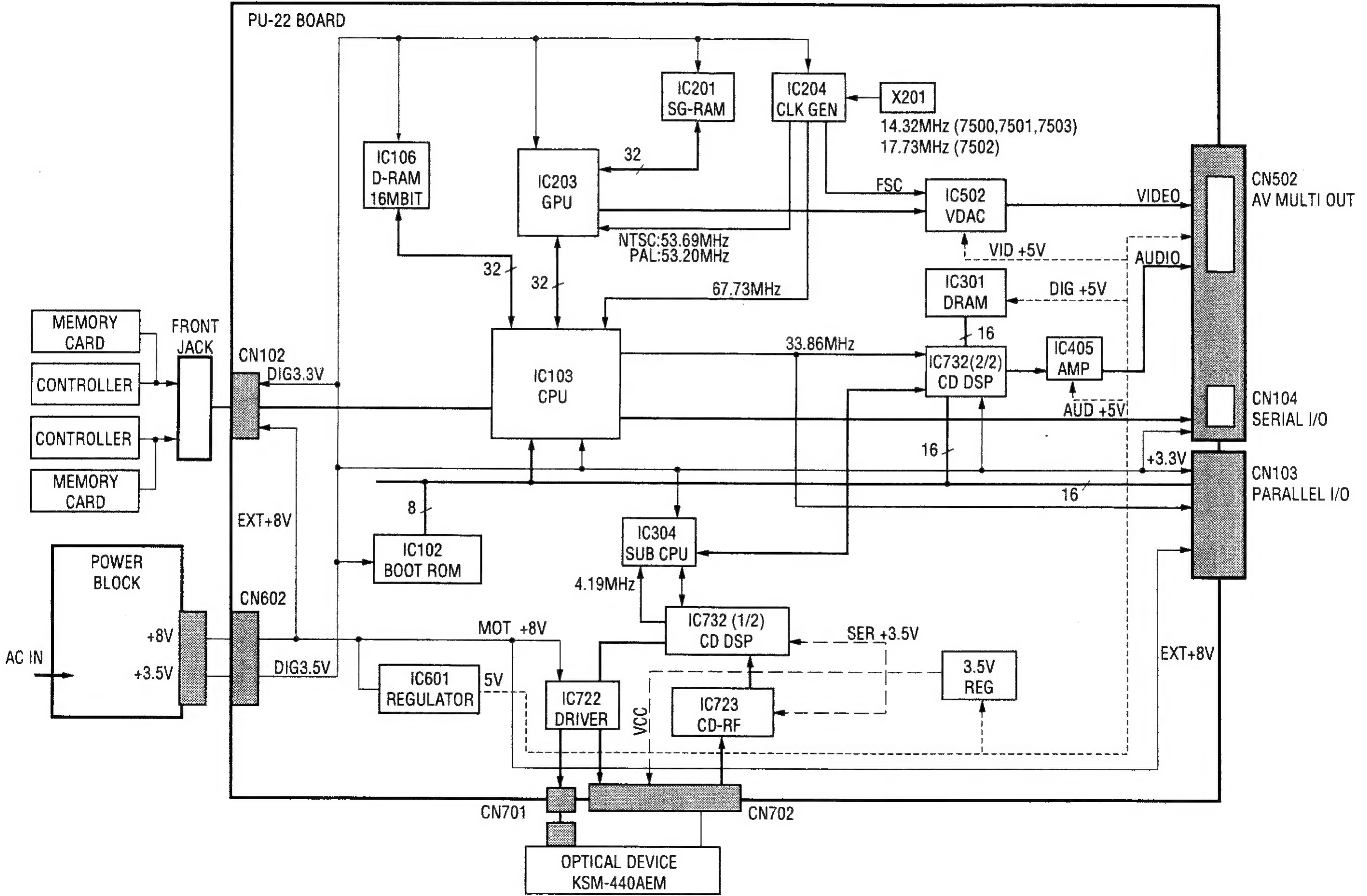
JARD (4/5)





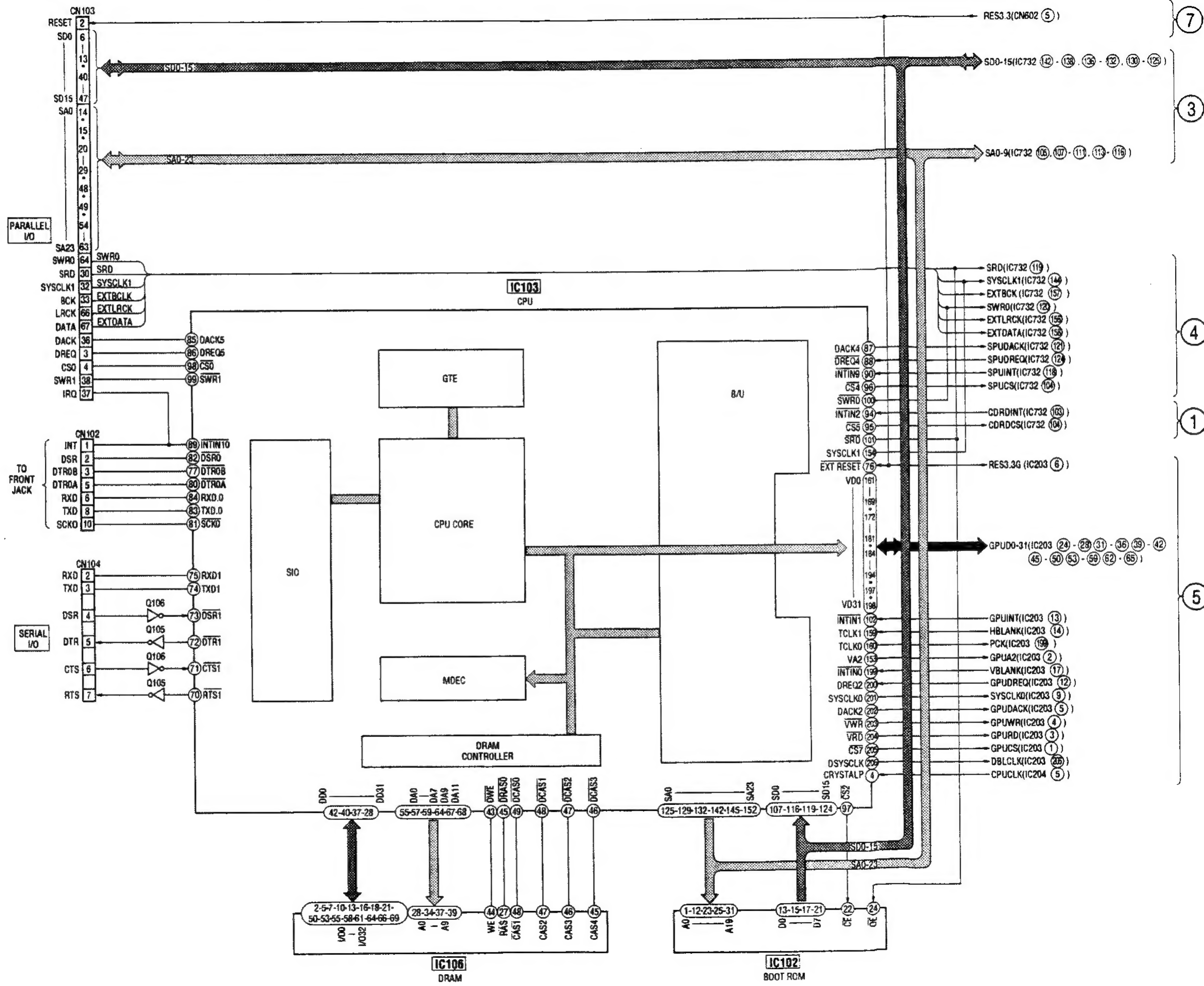
# SECTION 5 BLOCK DIAGRAMS

5-1. OVERALL BLOCK DIAGRAM



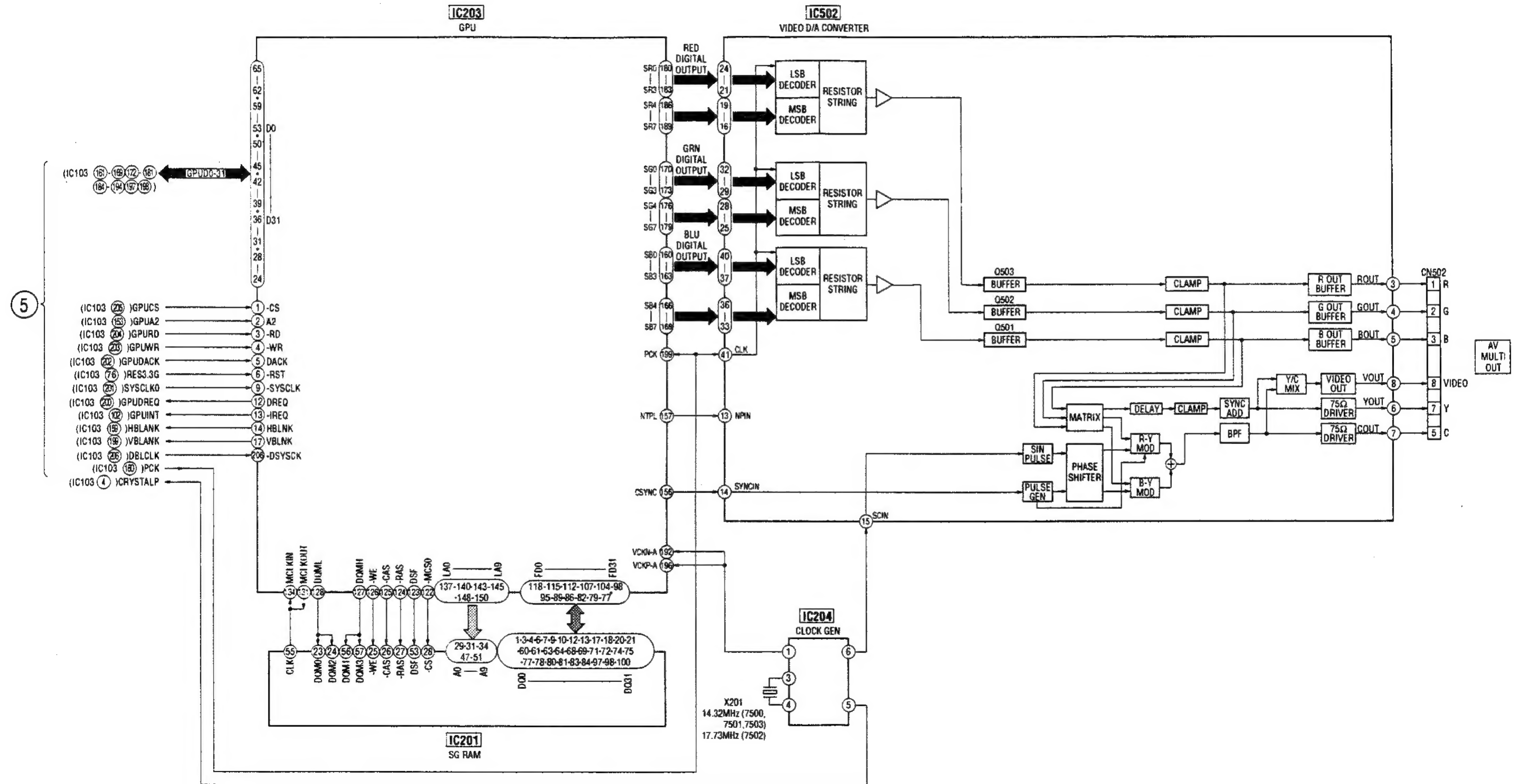


5-2. CPU BLOCK DIAGRAM



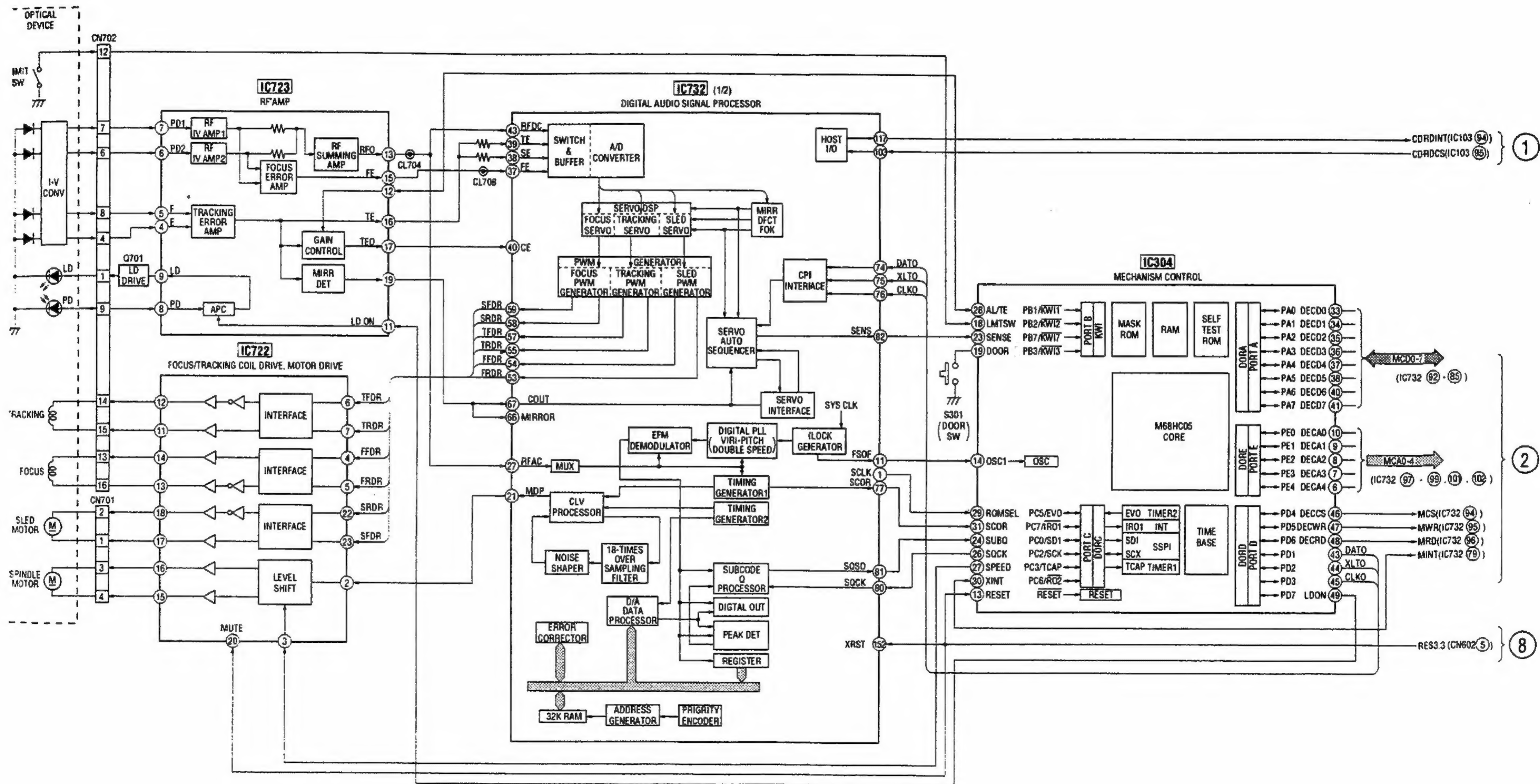


-3. VIDEO BLOCK DIAGRAM



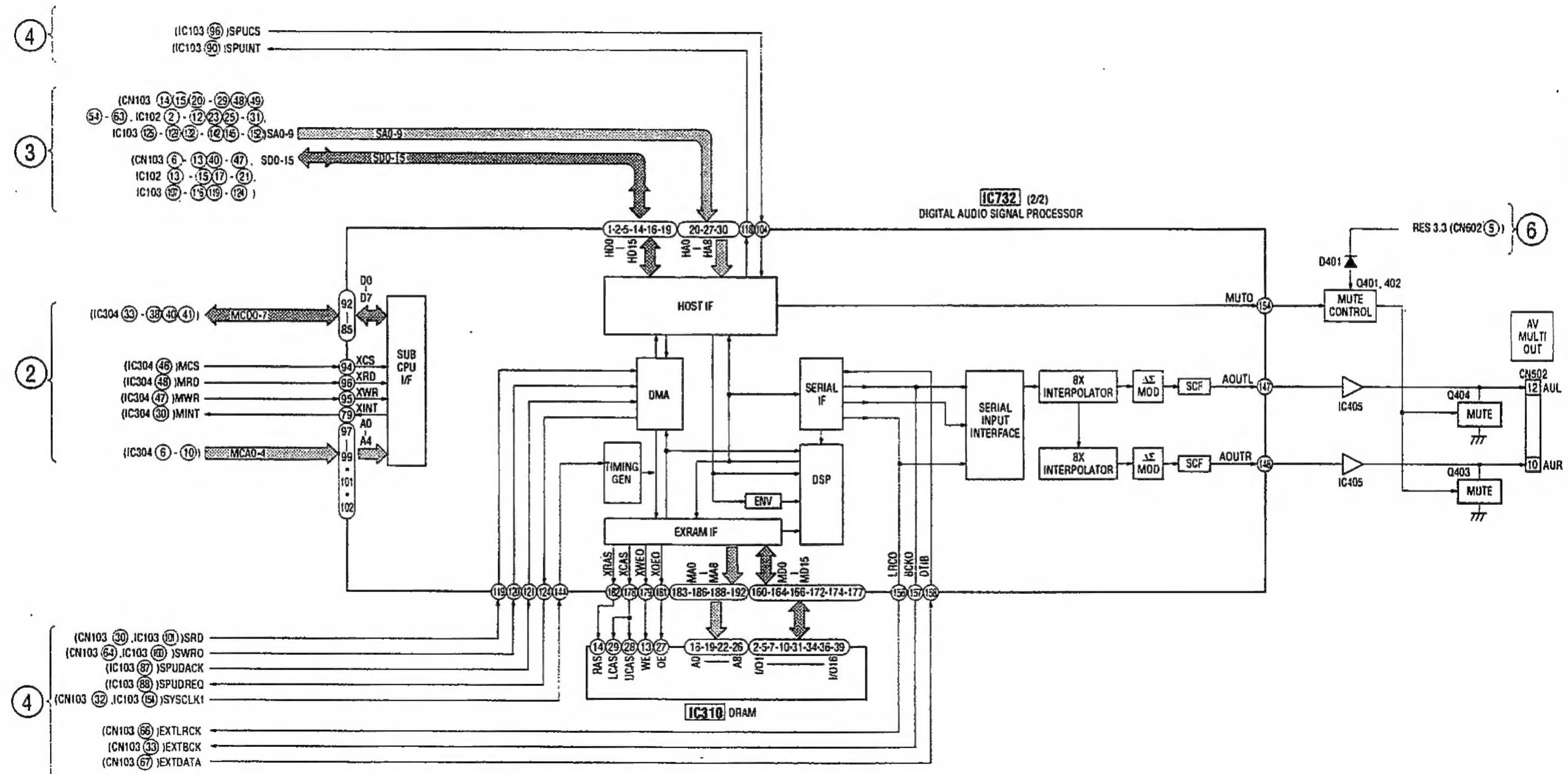


## 5. SERVO BLOCK DIAGRAM





# 5-4. AUDIO BLOCK DIAGRAM





**IC102**

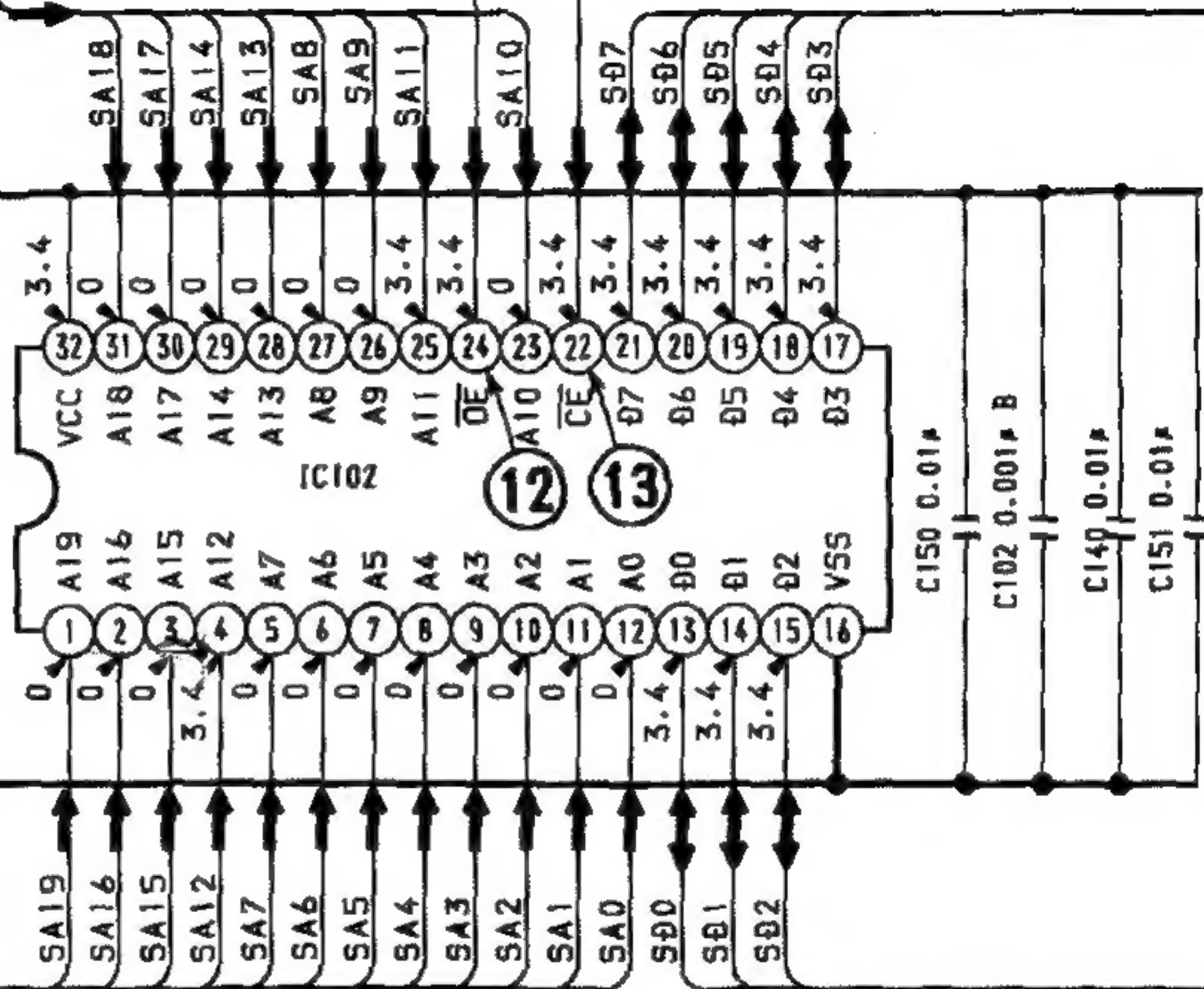
BOOT ROM

IC102

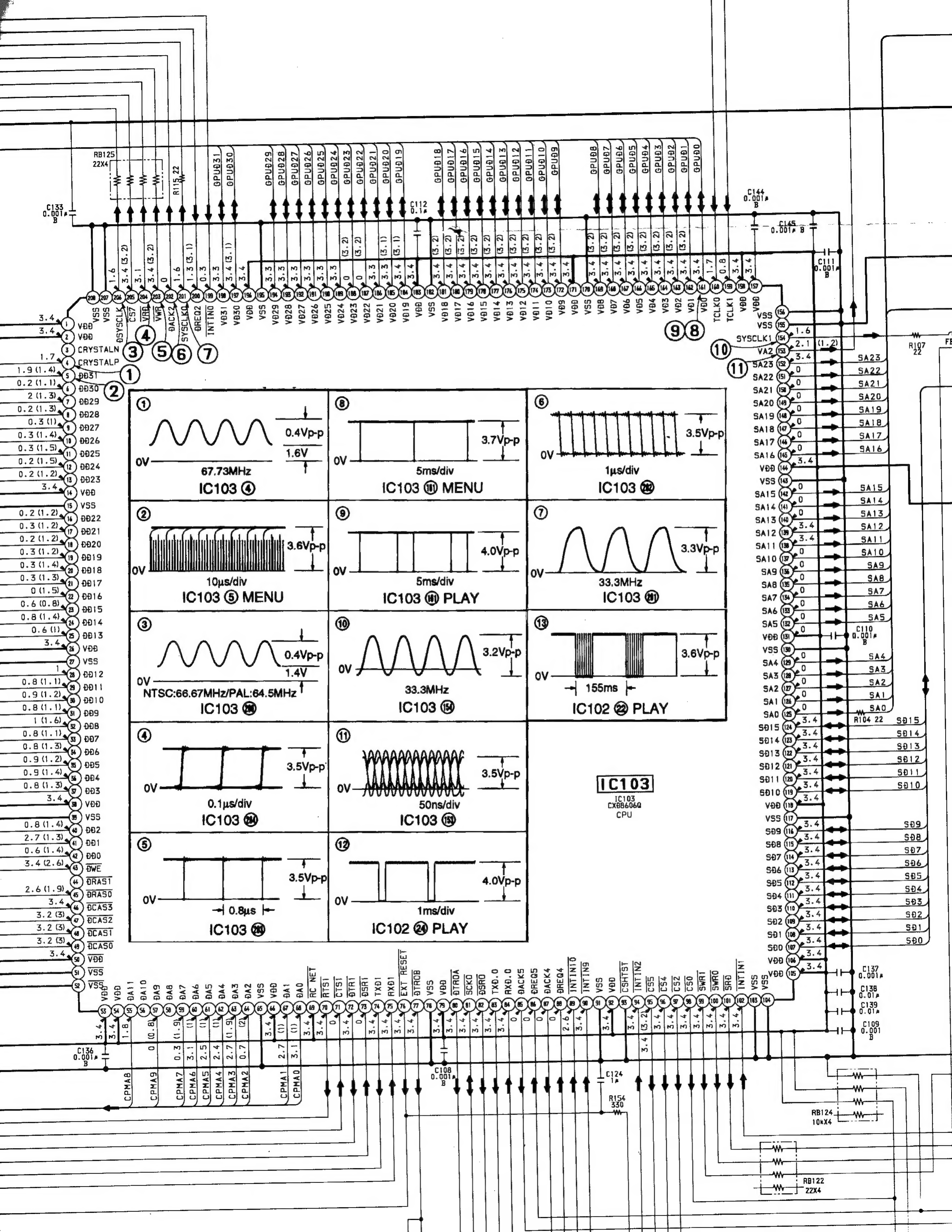
KM23V400000-15-KF5314J-1T (7500)

KM23V400000-15-KF5316J-1T (7501, 7503)

MSM534031E-10GS-KPR2 (7502)







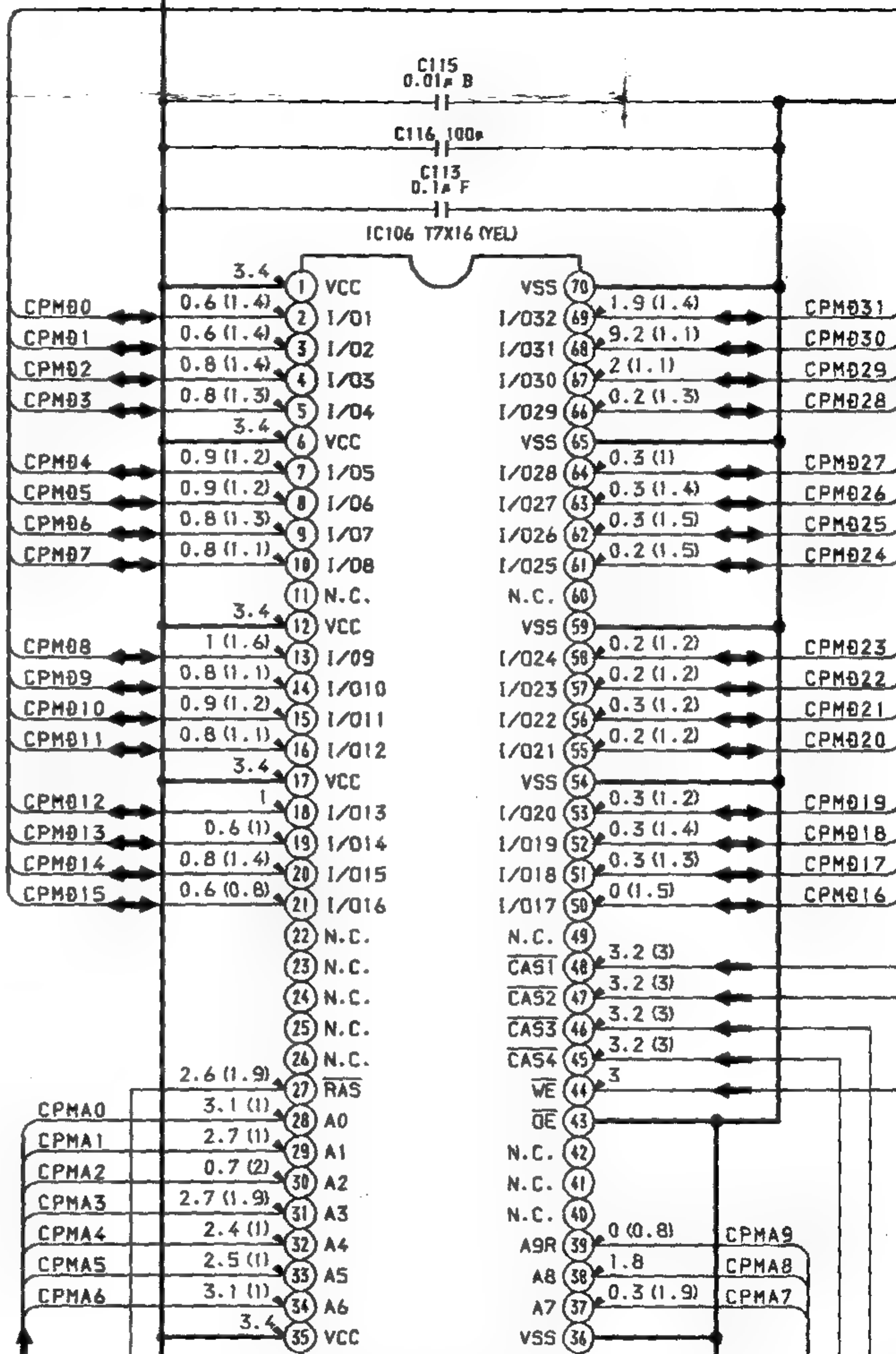


IC203 (206) (-BSYSCK) ← DBLCLK

IC204 (5) → CPUCLK

IC203 (65) (00)	GPU00	GPU00
IC203 (64) (01)	GPU01	GPU01
IC203 (63) (02)	GPU02	GPU02
IC203 (62) (03)	GPU03	GPU03
IC203 (59) (04)	GPU04	GPU04
IC203 (58) (05)	GPU05	GPU05
IC203 (57) (06)	GPU06	GPU06
IC203 (56) (07)	GPU07	GPU07
IC203 (55) (08)	GPU08	GPU08
IC203 (54) (09)	GPU09	GPU09
IC203 (53) (10)	GPU10	GPU10
IC203 (50) (11)	GPU11	GPU11
IC203 (49) (12)	GPU12	GPU12
IC203 (48) (13)	GPU13	GPU13
IC203 (47) (14)	GPU14	GPU14
IC203 (46) (15)	GPU15	GPU15
IC203 (45) (16)	GPU16	GPU16
IC203 (42) (17)	GPU17	GPU17
IC203 (41) (18)	GPU18	GPU18
IC203 (40) (19)	GPU19	GPU19
IC203 (39) (20)	GPU20	GPU20
IC203 (36) (21)	GPU21	GPU21
IC203 (35) (22)	GPU22	GPU22
IC203 (34) (23)	GPU23	GPU23
IC203 (33) (24)	GPU24	GPU24
IC203 (32) (25)	GPU25	GPU25
IC203 (31) (26)	GPU26	GPU26
IC203 (28) (27)	GPU27	GPU27
IC203 (27) (28)	GPU28	GPU28
IC203 (26) (29)	GPU29	GPU29
IC203 (25) (30)	GPU30	GPU30
IC203 (24) (31)	GPU31	GPU31

IC203 (6) (-RST) ← RES3.30



IC106

DRAM

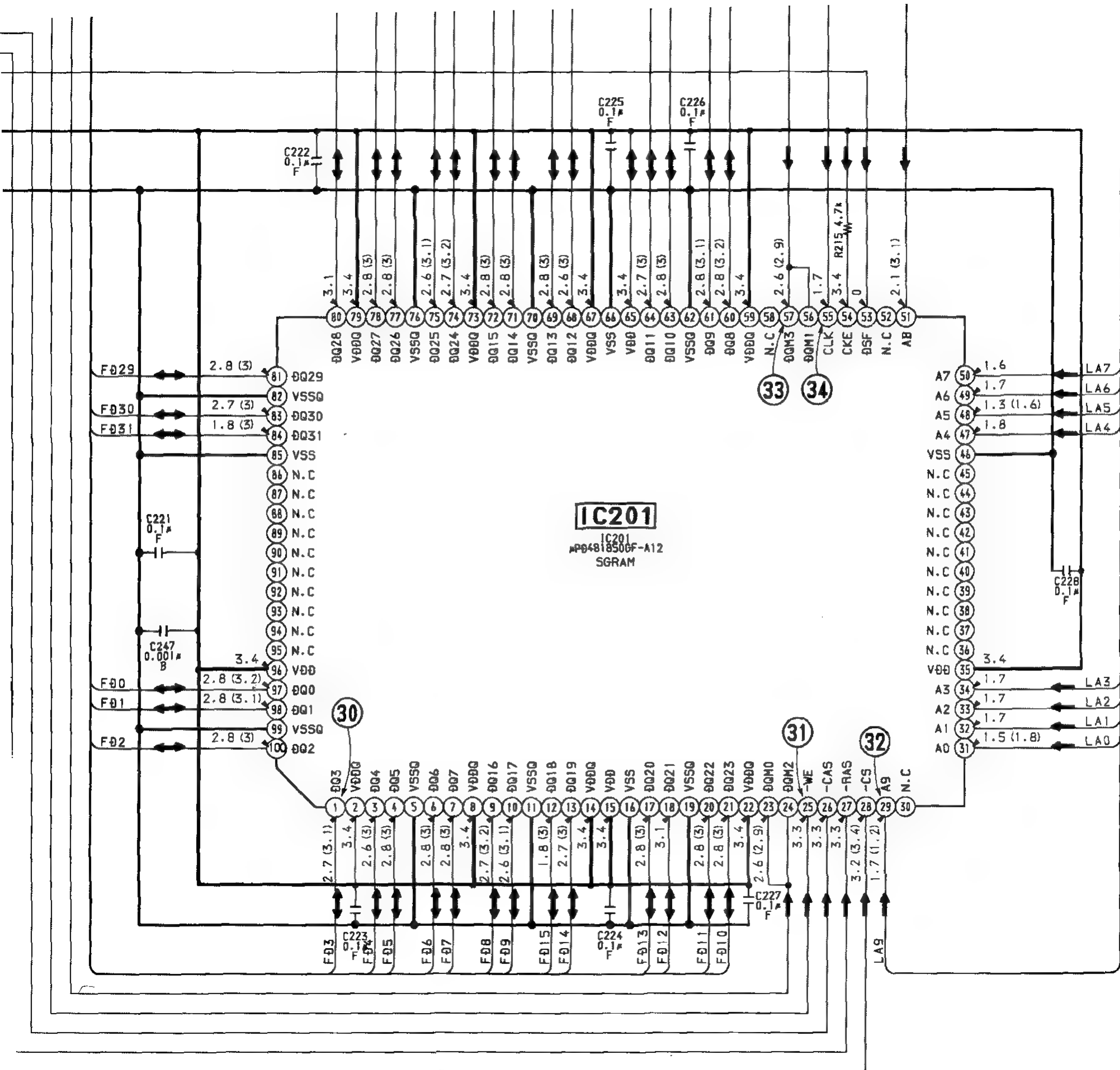
C118 0.001µF B

C122 0.001µF B

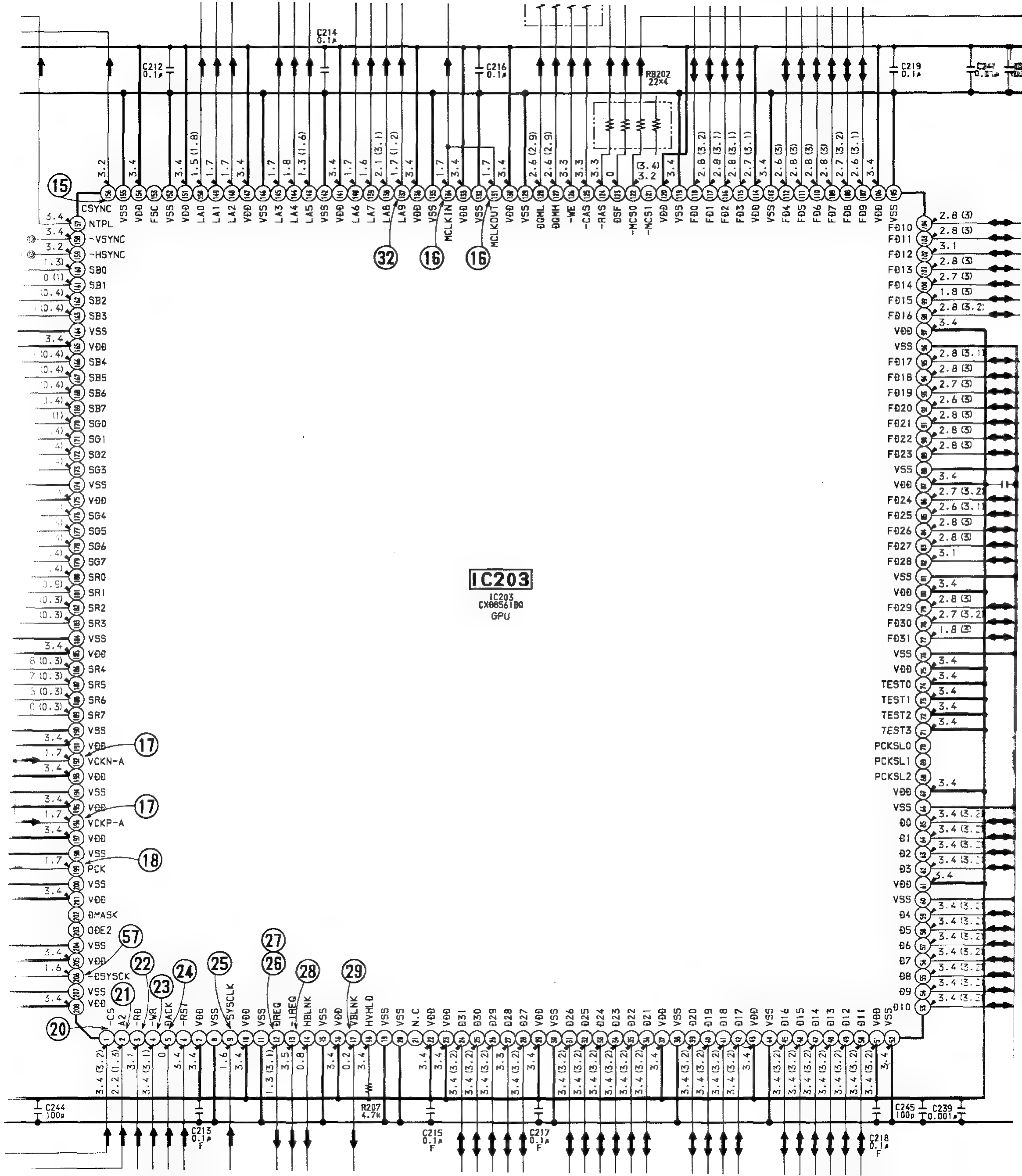
RB121 22X4

RB120 22X4

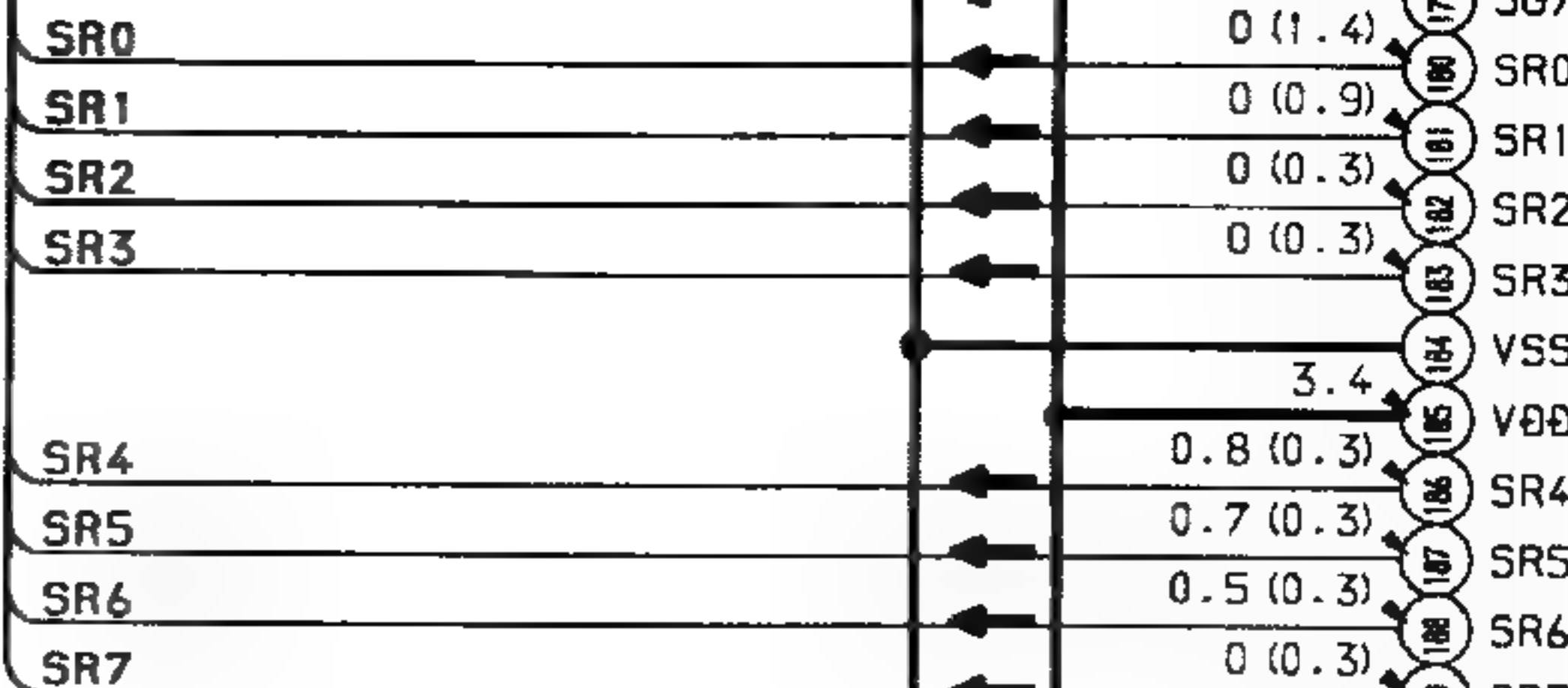




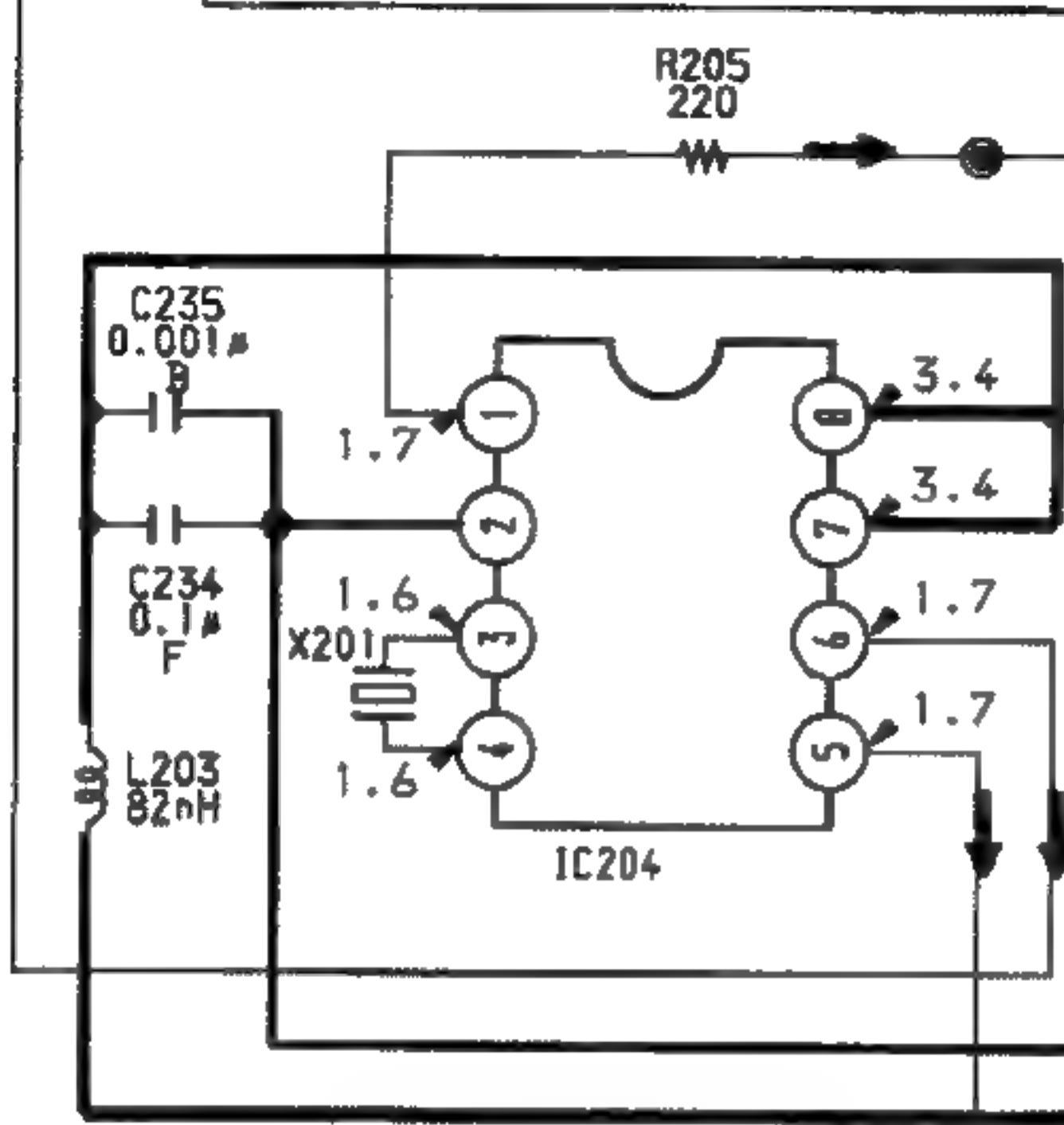








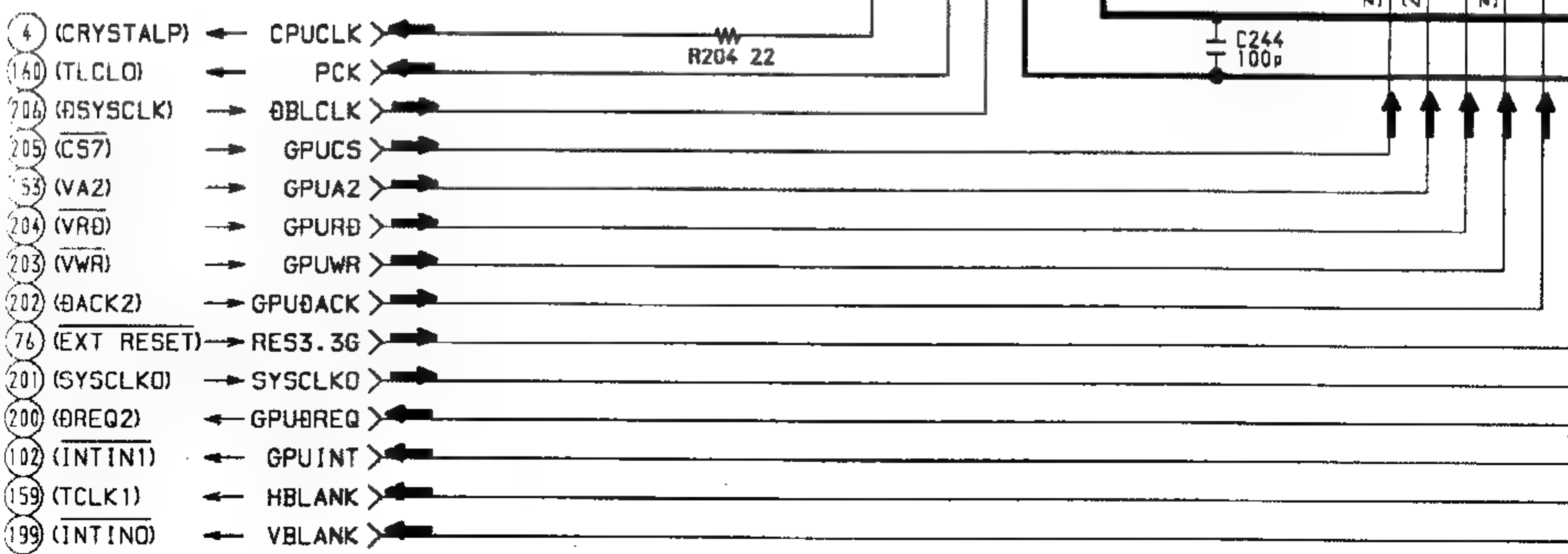
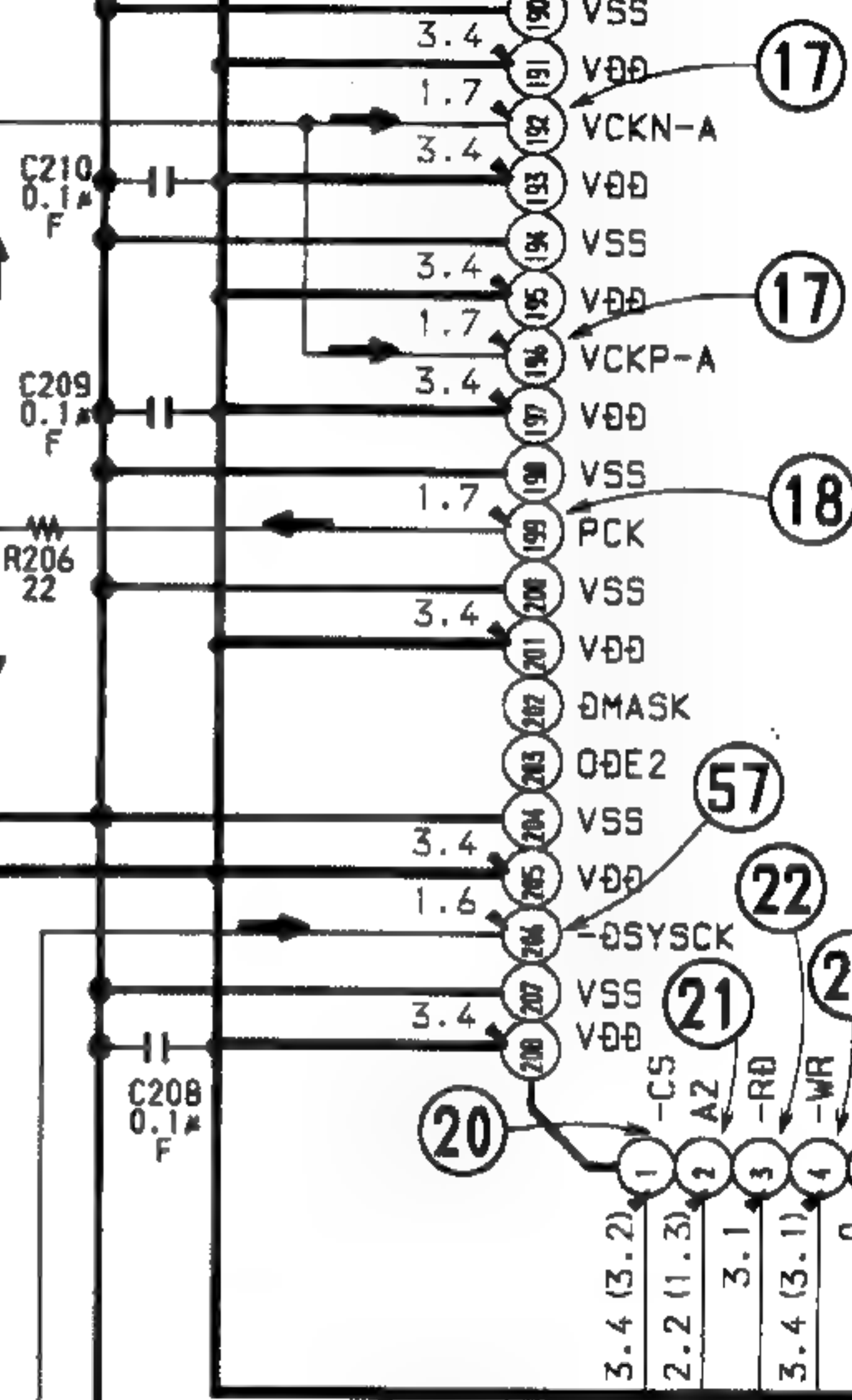
X201  
 14.32MHz (7500, 7501, 7503)  
 17.73MHz (7502)



**IC204**

IC204  
 CY20819L-500T (7500, 7501, 7503)  
 CY20819L-509T (7502)  
 CLOCK GEN

(58)

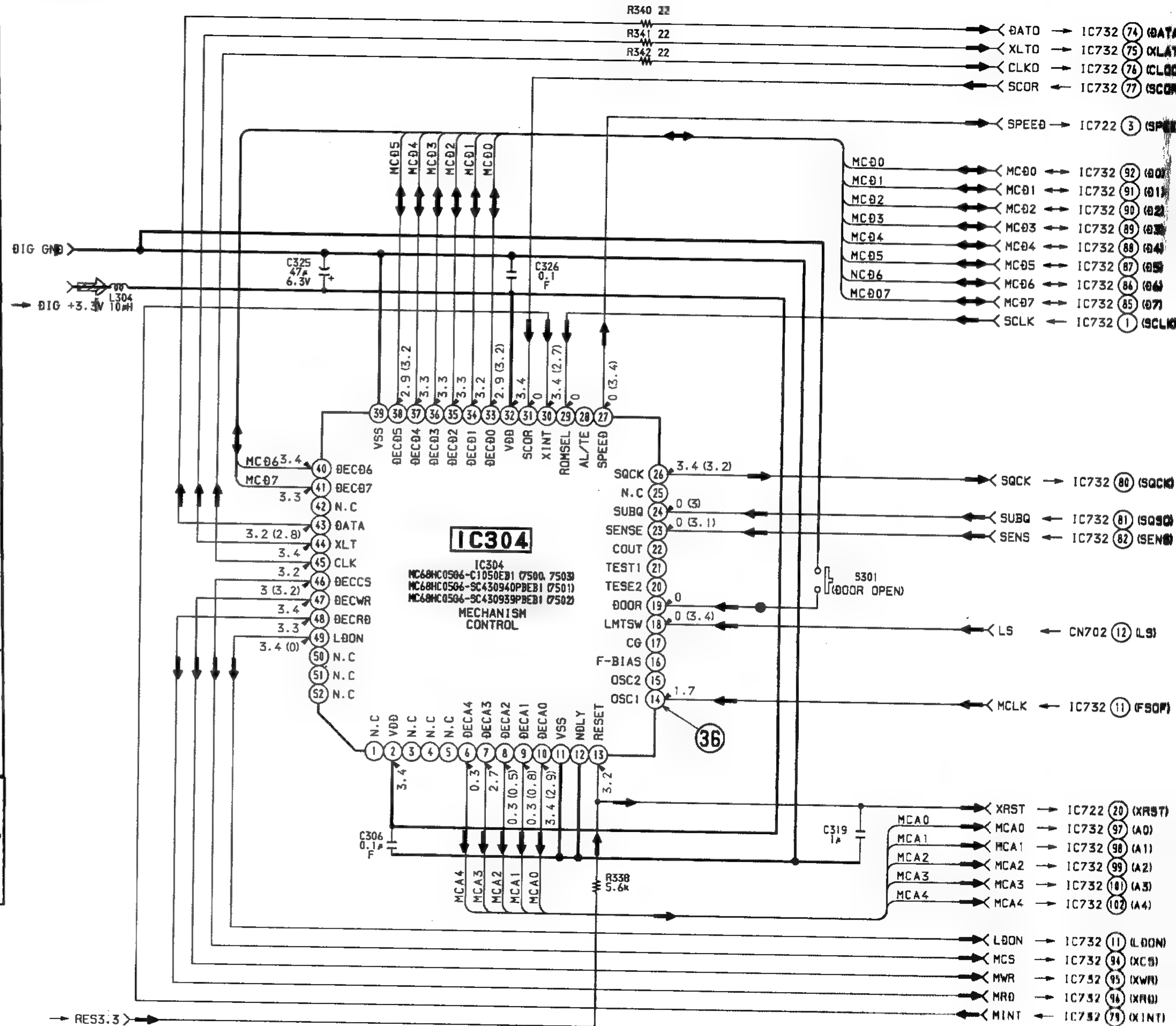
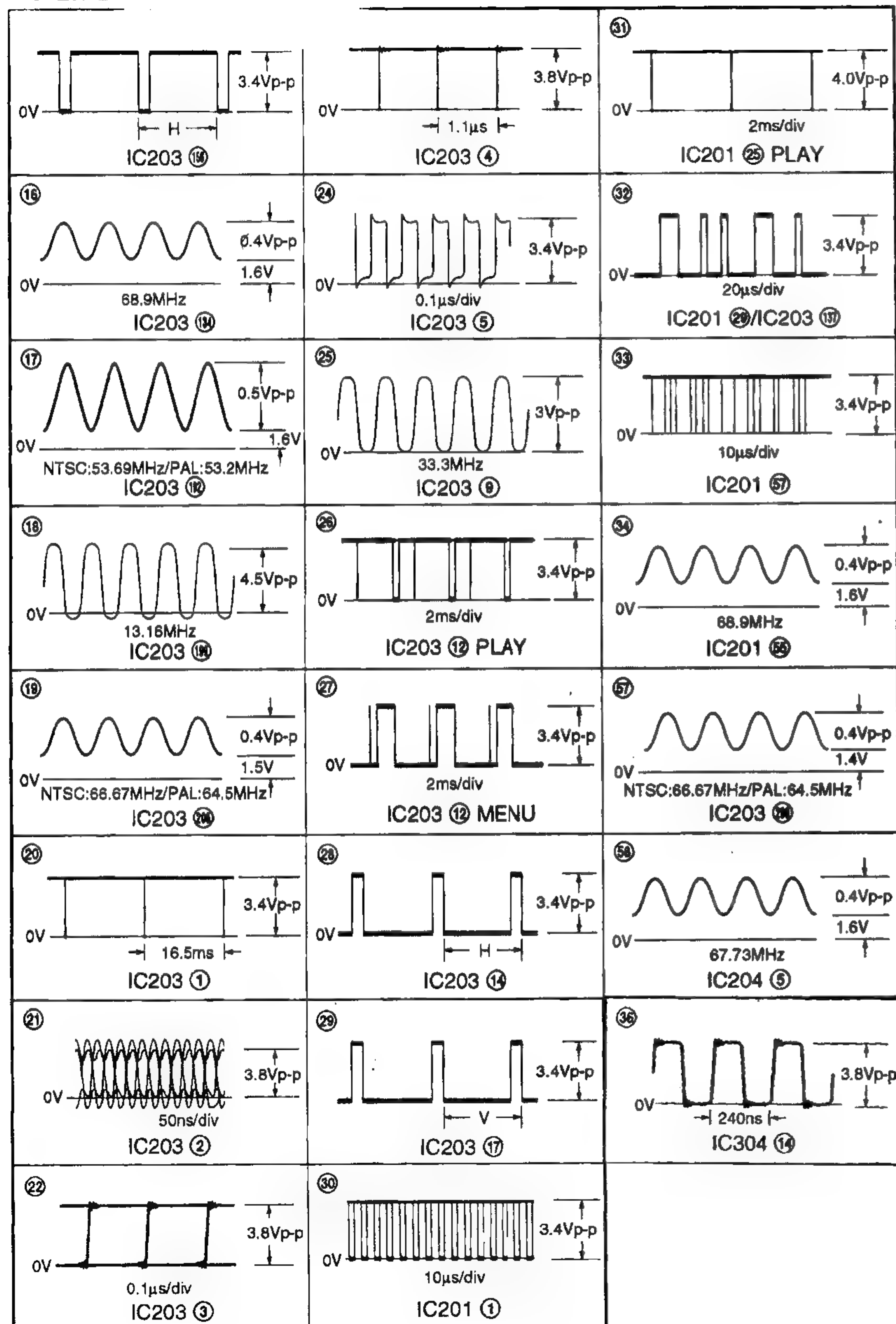




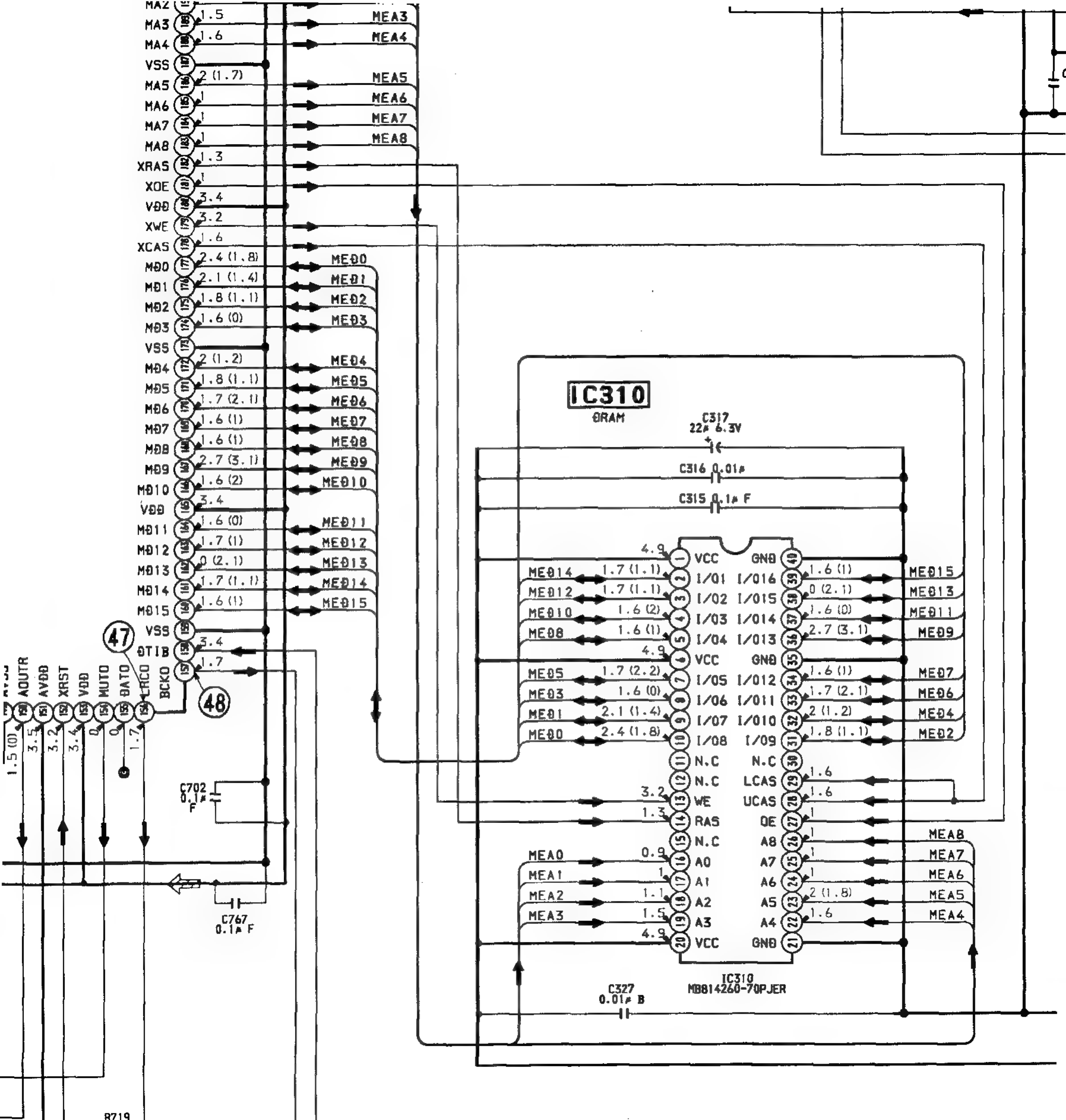
# 6-5. SCHEMATIC DIAGRAM (PU-22 (-11/-12/-21/-22/-32) BOARD (3/5))

## PU-22 BOARD (3/5)

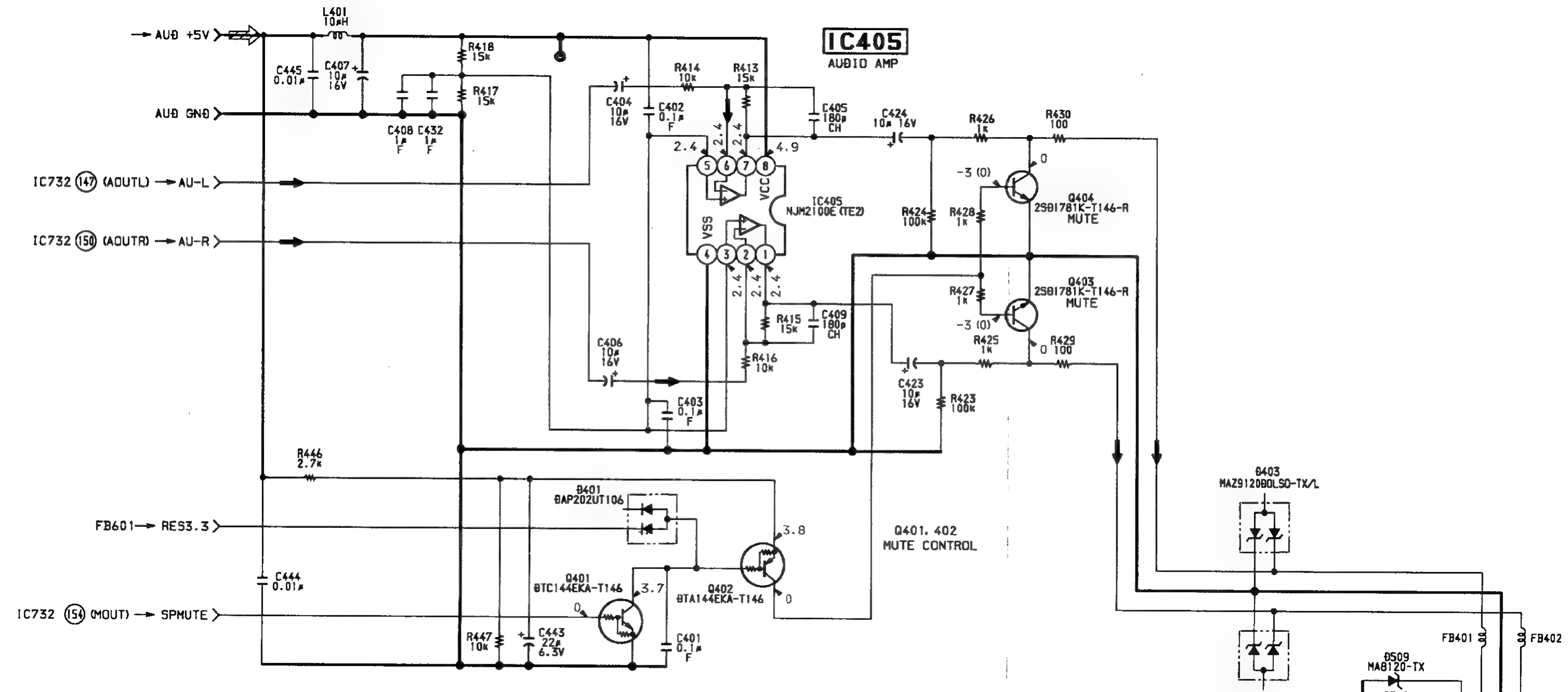
PU-22 B(



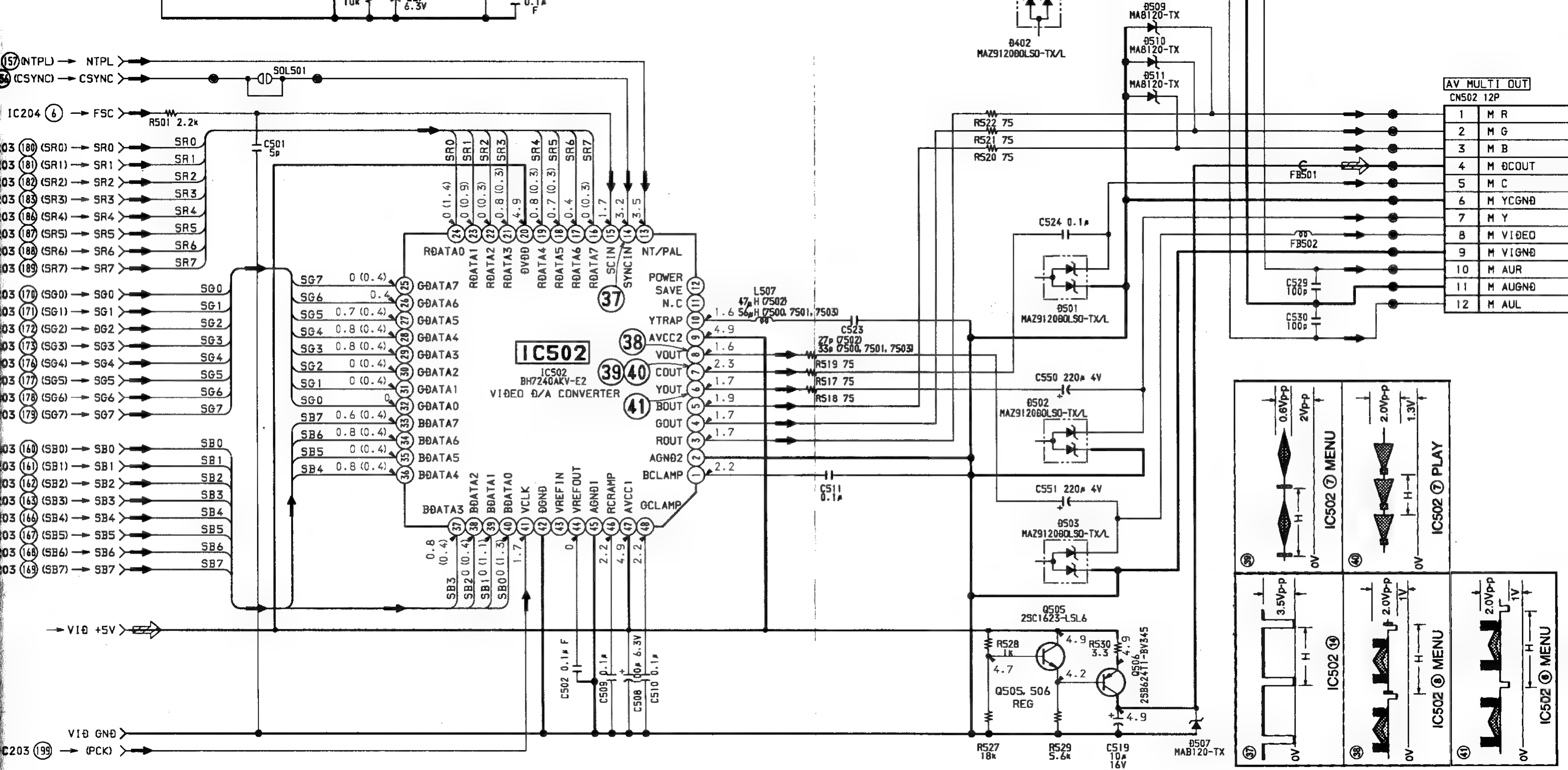




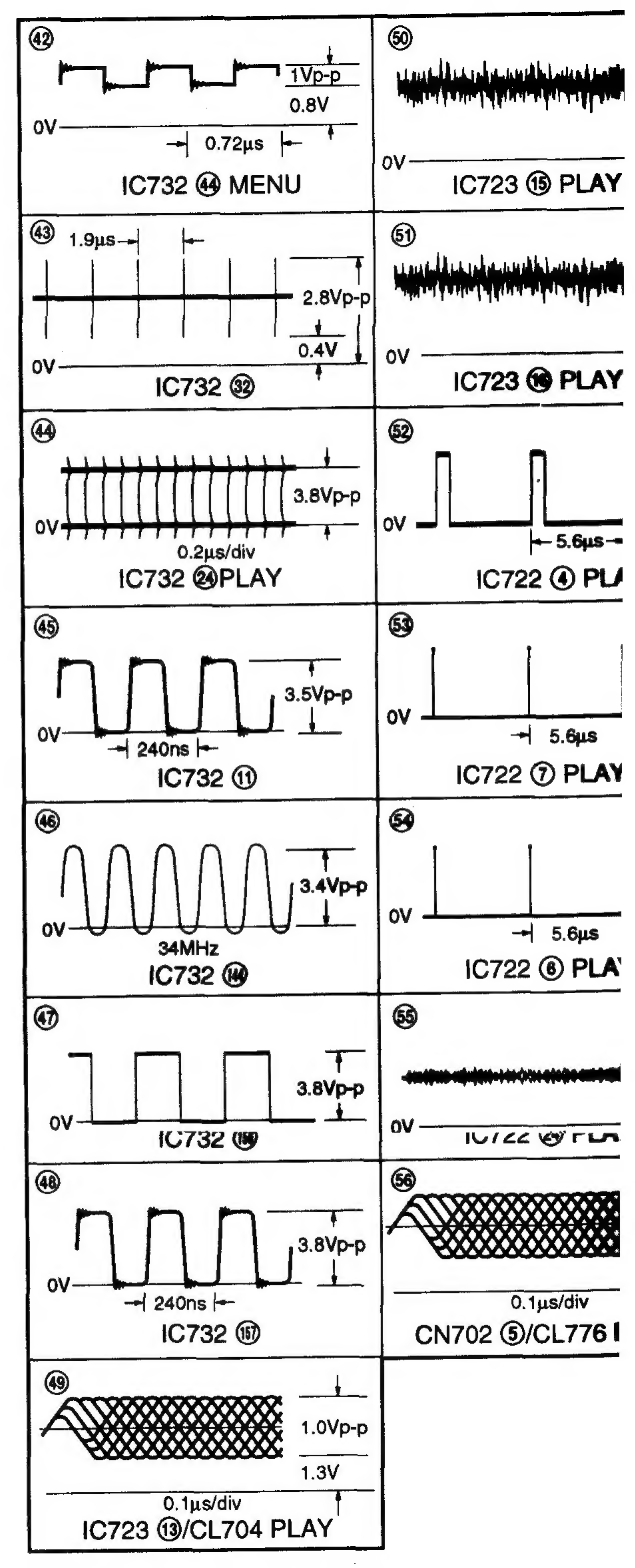
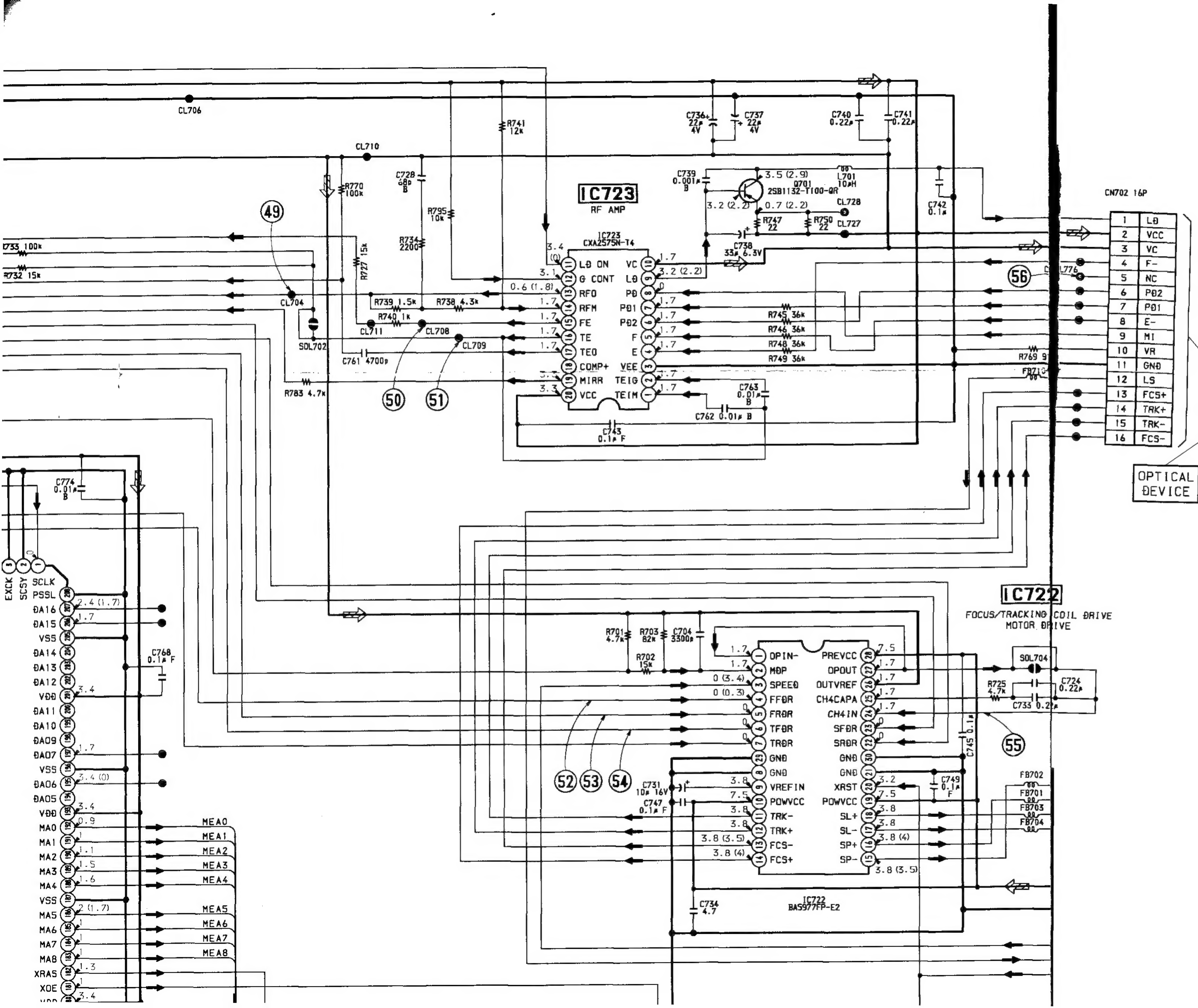




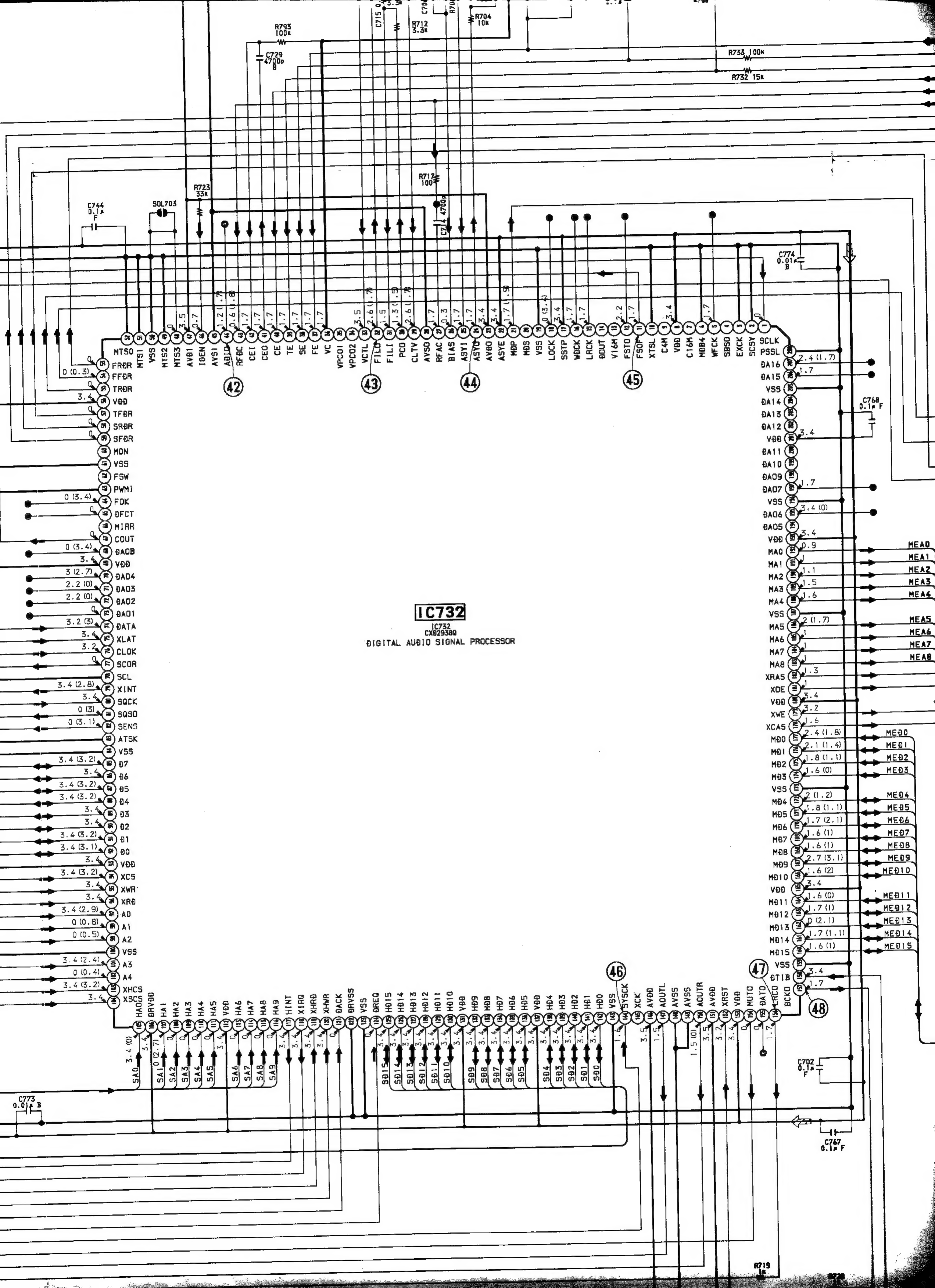




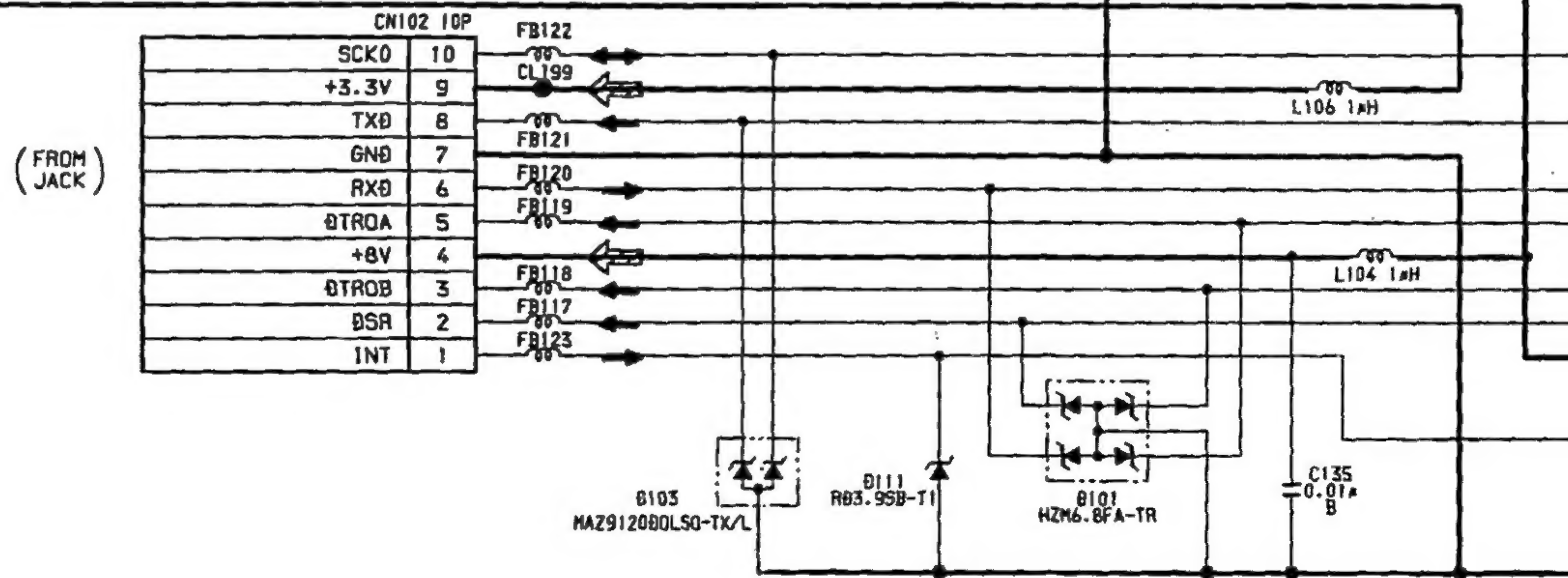
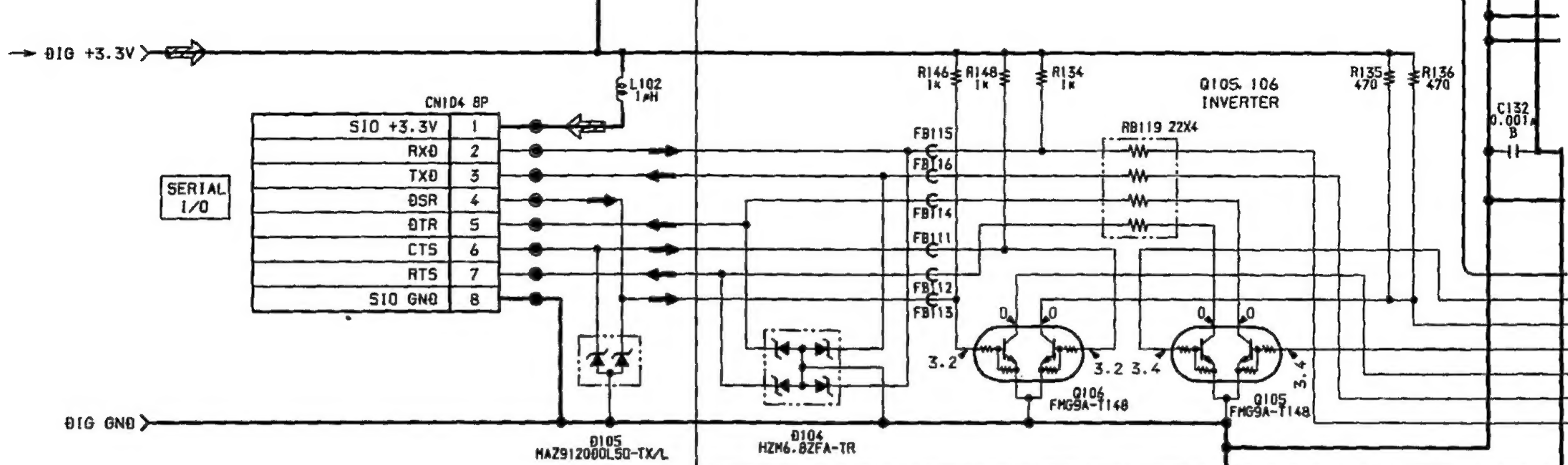












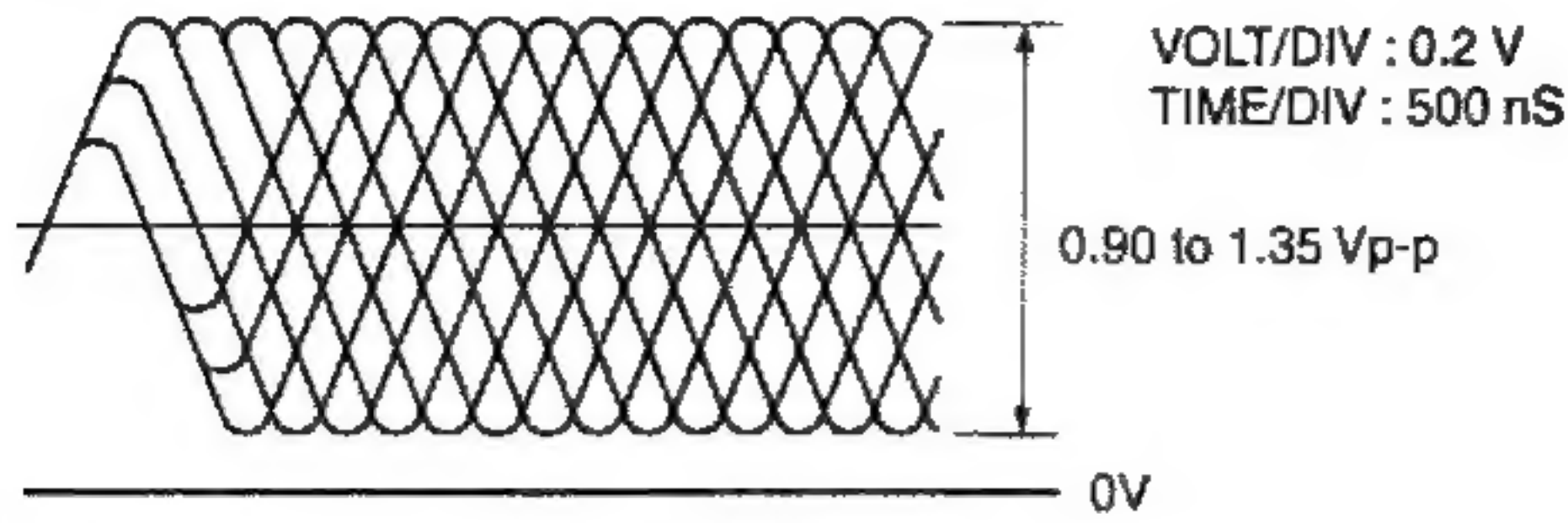


## SECTION 3 ADJUSTMENTS

### 3-1. CHECK SPECIFICATION

RF level 0.90 to 1.35 Vp-p (Check point : Between CL704 (HOT) and CL710 (VC).)

- RF signal waveform (eye pattern)



Use SCD-2700 DISC when measured RF level.  
Use the oscilloscope with input impedance more than 10 MΩ.

RF Jitter Below 9.0 nS (Measuring by KJM-6135S JITTER METER.)

Below 27.0 nS (Measuring by KJM-6235S JITTER METER.)

PP level  $1.1 \pm 0.6$  Vp-p (Check point : Between CL776 (HOT) and CL710 (VC).)

Use LPF ( $f_c = 10$  kHz)

Tracking level  $1.25 \pm 0.65$  Vp-p (Check point : Between CL709 (HOT) and CL710 (VC).)

Caution. Vc Line (CL710) do not make common use with GND line.

### Check Point for PU-22 Board.

